

SEMICONDUCTOR INTEGRATED CIRCUIT HAVING A PLURALITY OF  
CIRCUIT REGIONS WHERE DIFFERENT POWER SUPPLY VOLTAGES ARE  
USED AND METHOD OF MANUFACTURING THE SAME

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ABSTRACT OF THE DISCLOSURE

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A semiconductor integrated circuit has a first circuit region using a first power supply voltage and a second circuit region using a second power supply voltage different from the first power supply voltage. The first circuit region is manufactured by a first design rule in accordance with the first power supply voltage, and the

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second circuit region is manufactured by a second design rule in accordance with the second power supply voltage.